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PROFESSIONAL PROFILE:

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EDUCATION BACKGROUND:

Ph.D.: Faculty of Engineering and Computer (2007), Isfahan University of Technology, Isfahan, Iran

M.Sc.: Department of Electronic Eng., Shrif University of Technology(1997), Tehran, Iran.

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Dissertation title:

Analysis and Design of Current-Mode Companding Circuits, Examining their Nonlinear Behavior and Application in Data Converters



TEACHING AND TRAINING EXPERINCE:

Advance Integrated Circuit Data converter VLSI Electronics I, II, III

Electrical Circuits I, II

Microprocessor

Computer Architecture

Logic Circuit

Industrial Electronic

INTERESTS AND RESEARCH FIELDS:

Cicruit and System

RESEARCH ACTIVITIES:

Journals:

	R. Fani, E. Farshidi, E. Adib, A. Kosarian, "Analysis, Design and Implementation of a ZVT High
[1]	Step up DC/DC Converter with Continuous Input Current," IEEE Transactions on Industrial
	<i>Electronics,</i> vol. 19, no. 3, pp. 645-654, May 2020.
	R. Moradi, E. Farshidi and M. Soroosh, "Digital Calibration of Memory Errors in Passive
[2]	Sigma-Delta Modulator," IETE Research, Taylor& Francis, vol. 66, no. 10, pp. 14-21, Mar.
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	A. Mirali, M. Soroosh and E. Farshidi, "Ultra-Fast All-Optical Half Subtractor Based on
[3]	Photonic Crystal Ring Resonators," Journal of Optoelectronical Nanostructures, vol. 5, no. 1,
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	T. Ghanavati Nejad, E. Farshidi, H. Sjöland, A. Kosarian, "Voltage reference All-CMOS
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LANGUAGES:

PERSIAN: native	
ENGLISH: good	